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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/614,997	07/08/2003	Dennis Harold Burke JR.	T134951	8166
	7590 04/18/200 UMENTS INCORPO	EXAMINER		
P O BOX 655474, M/S 3999			CHUNG, PHUNG M	
DALLAS, TX 75265			ART UNIT	PAPER NUMBER
			2117	
SHORTENED STATUTOR	Y PERIOD OF RESPONSE	MAIL DATE	DELIVERY MODE	
3 MO	NTHS	04/18/2007	PAPER	

Please find below and/or attached an Office communication concerning this application or proceeding.

If NO period for reply is specified above, the maximum statutory period will apply and will expire 6 MONTHS from the mailing date of this communication.

	Application No.	Applicant(s)				
	10/614,997	BURKE ET AL.				
Office Action Summary	Examiner	Art Unit				
	Phung My Chung	2117				
The MAILING DATE of this communication app Period for Reply	ears on the cover sheet with the c	orrespondence address				
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.  - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.  - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.  - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).						
Status						
1) Responsive to communication(s) filed on 30 Ja	nuary 2007.					
• '-						
3) Since this application is in condition for allowar						
closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.						
Disposition of Claims						
4) Claim(s) 2-24 and 26-34 is/are pending in the application.						
4a) Of the above claim(s) is/are withdrawn from consideration.						
5) Claim(s) is/are allowed.						
6)⊠ Claim(s) <u>2-24 and 26-34</u> is/are rejected.						
7) Claim(s) is/are objected to.						
8) Claim(s) are subject to restriction and/or	election requirement.					
Application Papers						
9) The specification is objected to by the Examiner.						
10) The drawing(s) filed on is/are: a) accepted or b) objected to by the Examiner.						
Applicant may not request that any objection to the	drawing(s) be held in abeyance. See	e 37 CFR 1.85(a).				
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).						
11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.						
Priority under 35 U.S.C. § 119						
12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).  a) All b) Some * c) None of:						
1. Certified copies of the priority documents	have been received.					
2. Certified copies of the priority documents	s have been received in Applicati	on No				
3. Copies of the certified copies of the prior	3. Copies of the certified copies of the priority documents have been received in this National Stage					
application from the International Bureau	application from the International Bureau (PCT Rule 17.2(a)).					
* See the attached detailed Office action for a list of the certified copies not received.						
•						
Attachment(s)						
1) Notice of References Cited (PTO-892)  4) Interview Summary (PTO-413)						
2) Notice of Draftsperson's Patent Drawing Review (PTO-948)  Paper No(s)/Mail Date  Notice of Informal Patent Application						
3) Information Disclosure Statement(s) (PTO/SB/08)  Paper No(s)/Mail Date  5) Notice of Informal Patent Application 6) Other:						

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## Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371(c) of this title before the invention thereof by the applicant for patent.

The changes made to 35 U.S.C. 102(e) by the American Inventors Protection Act of 1999 (AIPA) and the Intellectual Property and High Technology Technical Amendments Act of 2002 do not apply when the reference is a U.S. patent resulting directly or indirectly from an international application filed before November 29, 2000. Therefore, the prior art date of the reference is determined under 35 U.S.C. 102(e) prior to the amendment by the AIPA (pre-AIPA 35 U.S.C. 102(e)).

2. Claims 2-6, 8-19, 21-24, 26-30 and 32-34 are rejected under 35 U.S.C. 102(e) as being anticipated by Kittross et al (6,681,351).

As per claims 12-14, 16-17, 19 and 21-24, Kittross et al disclose an apparatus for testing at least one mixed signal semiconductor device, comprising:

At least one processor (26)(col. 14, lines 3-5); and

At least one device tester unit (20) coupled to the processor, wherein the processor is configured to: Execute a first test for the at least one mixed signal semiconductor device;

Prepare execution of a second test for the at least one mixed signal semiconductor device concurrently with the executing of the first test;

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Process test data resulting from the first test; and

Execute the second test concurrently with the processing of the test data.

These functions of the processor are read on the system of Kittross et al. (See col. 12, line 1 to col. 14, line 36).

As per claim 15, Kittross et al further disclose wherein the mixed signal semiconductor testing is performed by a single processor. (See lines 3-5).

As per claim 18, Kittross et al further disclose wherein the first and second tests are configured for one or more of wafer testing and package testing of the mixed signal semiconductor device. (See col. 1, lines 46-67 to col. 2, lines 1-7).

As per claims 2-4, 6, 8-11, 26-28, 30 and 32-34, these method claims are rejected under similar rationale as set forth in the system claims 12-14-17, 19 and 21-24.

As per claims 5 and 29, these method claims are rejected under similar rationale as set forth in the system claim 18.

## Claim Rejections - 35 USC § 103

- 3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
  - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

This application currently names joint inventors. In considering patentability of the claims under 35 U.S.C. 103(a), the examiner presumes that the subject matter of the various claims was commonly owned at the time any inventions covered therein

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were made absent any evidence to the contrary. Applicant is advised of the obligation under 37 CFR 1.56 to point out the inventor and invention dates of each claim that was not commonly owned at the time a later invention was made in order for the examiner to consider the applicability of 35 U.S.C. 103(c) and potential 35 U.S.C. 102(e), (f) or (g) prior art under 35 U.S.C. 103(a).

4. Claims 7, 20 and 31 are rejected under 35 U.S.C. 103(a) as being unpatentable over Kittross et al (6,681,351).

As per claim 20, the teaching of Kittross et al have been discussed above. They do not disclose wherein the interpreted software language is interactive test pascal. However, it would have been a mater of design choice to a person of ordinary skill in the art, at the time the invention was made, to interpreted software language as interactive pascal as desired if needed. This is because Kittross et al already disclose the programming language is C or C++ programming language (col. 1, lines 48-49) and various changes or modification can be made in the system of Kittross et al. (col. 13, lines 54-59).

As per claims 7 and 31, these method claims are rejected under similar rationale as set forth in the system claim 20.

5. Applicant's arguments with respect to claims 1-34 have been considered but are moot in view of the new ground(s) of rejection.

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6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Phung My Chung whose telephone number is 571-272-3818. The examiner can normally be reached on Monday to Thursday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jacques Louis-Jacques can be reached on 571-272-6962. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Phung My Chung

Primary Patent Examiner

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